

## IN THE CLAIMS

Please amend the claims as follows:

1. (original) A module for reading data carriers, with a processor arrangement and a reading unit,
  - wherein the module is designed for incorporation in a data processing device,
  - wherein addressable coded data are stored on the data carrier, and —
  - wherein the processor arrangement comprises a decoding function and is for this purpose designed for
    - receiving a request, characterized by an identifier, for decoded data which are stored in coded form on the data carrier,
    - controlling the reading unit such that the requested data, defined by a start address, are read in the coded form from the data carrier,
    - converting the coded data into decoded data by means of the decoding function, and
    - making the decoded data, characterized by the identifier, available.
2. (original) A module as claimed in claim 1, characterized in that the processor arrangement is designed for characterizing the decoded data by means of the current address from which the coded data were read from the data carrier.
3. (original) A module as claimed in claim 1, characterized in that the processor arrangement is designed for receiving the start address immediately along with the request.

4. (original) A module as claimed in claim 1, characterized in that the module comprises a memory arrangement,

- wherein said memory arrangement is designed for storing table of contents information of the data carrier, and
- wherein the processor unit is designed for deriving the start address from the request by using the table of contents information.

5. (original) A module as claimed in claim 1, characterized in that the processor arrangement is designed for receiving the request characterized by an identifier via a first serial bus and for making available the decoded data characterized by the identifier via a second serial bus.

6. (original) A module as claimed in claim 1, characterized in that the module comprises a memory arrangement, and in that the processor arrangement is designed for loading a decoding program from the memory arrangement, which program carries out the decoding function on a programmable processor.

7. (original) A module as claimed in claim 1, characterized in that the data processing device is a car radio or a navigation system or a combined car radio/navigation system.

8. (currently amended) A data carrier playback device, in which a module according to ~~one of the claims 1-7~~ claim 7 is incorporated.